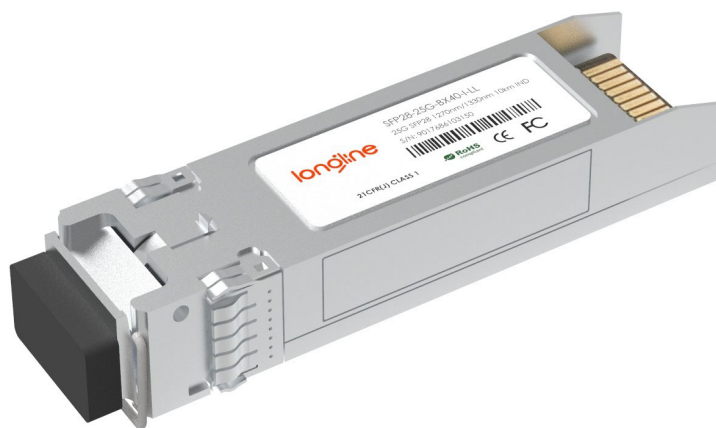


# 25GBASE- SFP28 1270nmTX/1310nmRX 40km Industrial DOM Transceiver

SFP28-25G-BX40-I-LL



## Applications

- 25GBASE-ER
- CPRI Option 10/e CPRI

## Features

- Up to 40 km Transmission Distance
- LC Single Connector

## Standards

- SFF-8472
- SFF-8402
- SFF-8432

- SFF-8431
- CEI-28G-VSR

- Single 3.3V  $\pm$  5% Power Supply
- Compliant with SFF-8472

## Description

The SFP28 transceiver is designed for use in Ethernet/eCPRI/ CPRI links up to 25Gb/s data rate and up to 40km link length. They are compliant with SFF8472, SFF-8402, SFF-8432 and applicable portions of SFF-8431. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

## Product Specifications

### I. Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
<b>Storage Temperature</b>	$T_S$	-40	85	°C
<b>Supply Voltage</b>	$V_{CC}$	-0.3	3.6	V
<b>Relative Humidity</b>	RH	0	95	%

### II. Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
<b>Operating Case Temperature Range</b>	$T_C$	-40		85	°C
<b>Power Supply Voltage</b>	$V_{CC}$	3.135	3.3	3.465	V
<b>Bit Rate</b>	BR		25.78125		Gb/s
<b>Max Supported Link Length</b>	L			40	km

### III. Electrical Characteristics (T<sub>c</sub>=-40°C to 85°C and V<sub>cc</sub>= 3.135 to 3.465V)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
<b>Supply Voltage</b>	V <sub>CC</sub>	3.14	3.3	3.46	V	
<b>Supply Current</b>	I <sub>CC</sub>			360	mA	
<b>Transmitter</b>						
<b>Input Differential Impedance</b>	R <sub>in</sub>		100		Ω	
<b>Single Ended Data Input Swing</b>	V <sub>in</sub>	90		450	mVp-p	
<b>Transmit Disable Voltage</b>	V <sub>DIS</sub>	2		V <sub>CCHOST</sub>	V	
<b>Transmit Enable Voltage</b>	V <sub>EN</sub>	V <sub>EE</sub>		V <sub>EE</sub> +0.8	V	
<b>Transmit Fault Assert Voltage</b>	T <sub>FA</sub>	2.2		V <sub>CCHOST</sub>	V	
<b>Transmit Fault De-Assert Voltage</b>	V <sub>FDA</sub>	V <sub>EE</sub>		V <sub>EE</sub> +0.4	V	
<b>Receiver</b>						
<b>Single Ended Data Output Swing</b>	V <sub>OD</sub>	200		450	mVp-p	
<b>LOS Fault</b>	V <sub>LOSFT</sub>	2.2		V <sub>CCHOST</sub>	V	
<b>LOS Normal</b>	V <sub>LOSNR</sub>	V <sub>EE</sub>		V <sub>EE</sub> +0.4	V	

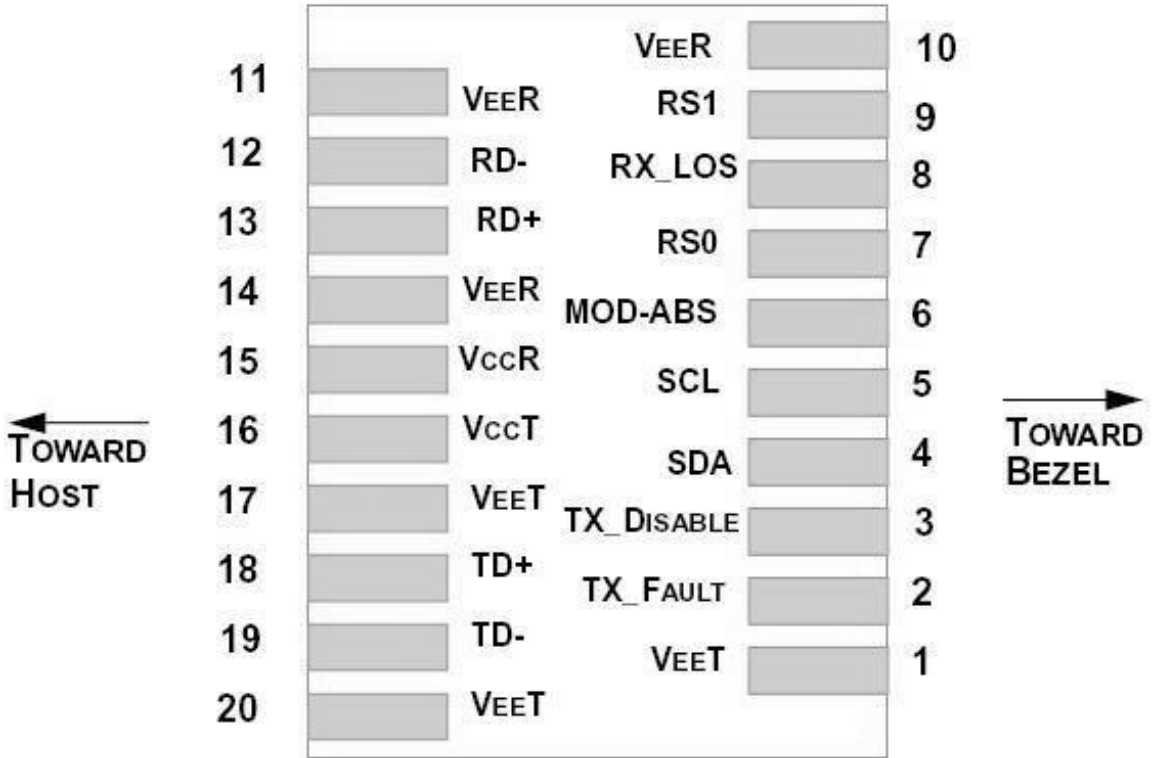
**IV. Optical Parameters** ( $T_{OP} = 0$  to  $70$  °C,  $V_{CC} = 3.00$  to  $3.60$  Volts)

Parameter	Symbol	Min.	Typical	Max.	Unit	Note
<b>Transmitter</b>						
<b>Center Wavelength</b>	$\lambda$	1260	1270	1280	nm	1
		1300	1310	1320	nm	2
<b>Side Mode Suppression Ratio</b>	SMSR	30			dB	
<b>Optical Output Power</b>	$P_{av}$	0		6	dBm	
<b>Extinction Ratio</b>	ER	4			dB	
<b>Transmitter and Dispersion Penalty</b>	TDP			1.5	dB	
<b>Average Launch Power of OFF Transmitter</b>	$P_{OFF}$			-30	dBm	
<b>Receiver</b>						
<b>Center Wavelength</b>	$\lambda$	1300	1310	1320	nm	1
		1260	1270	1280	nm	2
<b>Overload</b>				-6	dBm	
<b>Receiver Power</b>	$P_{av}$	-18			dBm	3
<b>Assert LOS</b>	$LOS_A$	-30			dBm	
<b>De-Assert LOS</b>	$LOS_D$			-20	dBm	
<b>LOS Hysteresis</b>		0.5			dB	

**Note:**

1. RTX330-238.
2. RTX330-239.
3. Measured with 25.78125Gb/s, PRBS 231-1, NRZ, ER>4dB, BER<5E-5.

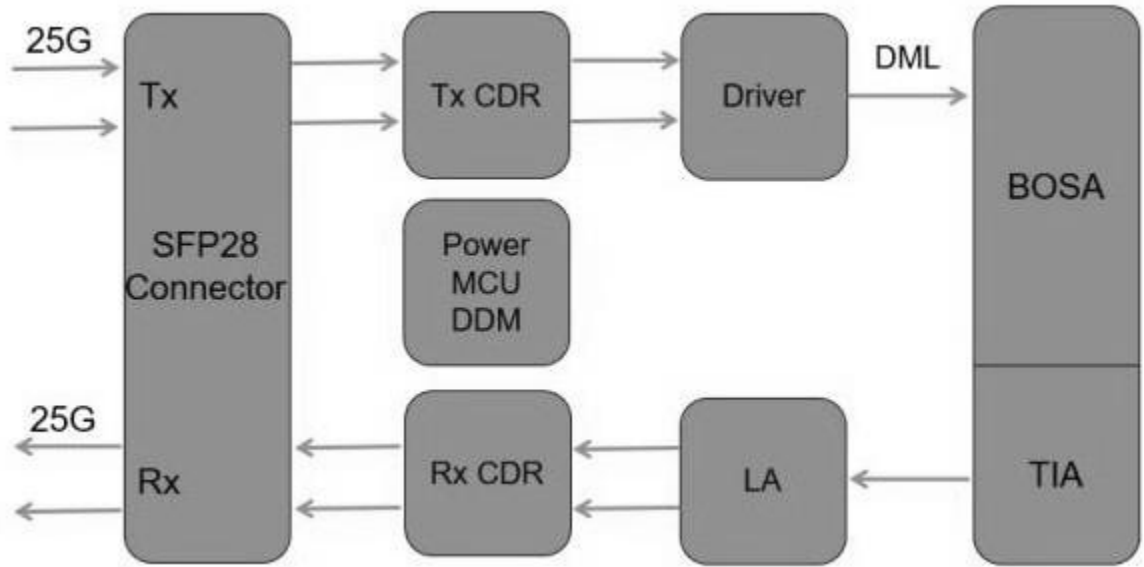
## V. Pin Assignment



## VI. Transceiver Pin Descriptions

Pin No.	Symbol	Logic	Description
1,17,20	$V_{EE}T$		Connected to signal ground on the host board
2	TX Fault	LVTTTL Output	Module transmitter fault output
3	TX Disable	LVTTTL Input	Module transmitter disable control
4	SDA	LVTTTL Input/Output	2-wire serial interface data
5	SCL	LVTTTL Input/Output	2-wire serial interface clock
6	MOD-ABS		Module absent (connected to Module ground)
7	RS0	LVTTTL Input	Rate select 0 (Rx) :Low=CDR Bypass ; High=CDR Select
8	LOS	LVTTTL Output	Receiver loss of signal
9	RS1	LVTTTL Input	Rate select 1 (Tx) :Low=CDR Bypass ; High=CDR Select
10,11,14	$V_{EE}R$		Connected to signal ground on the host board.
12	RD-	CML Output	Receiver inverted data output, internally AC coupled and terminated
13	RD+	CML Output	Receiver non-inverted data output, internally AC coupled and terminated.
15	$V_{CC}R$		Receiver power 3.3V supply
16	$V_{CC}T$		Transmitter power 3.3V supply
18	TD+	CML Input	Transmitter non-inverted data input, internally AC coupled and terminated.
19	TD-	CML Input	Transmitter inverted data Input, internally AC coupled and terminated.

## VII. Principle Diagram



## VIII. Mechanical Dimensions:

