

25GBASE- SFP28 1310nmTX/1270nmRX 40km Industrial DOM Transceiver

SFP-25GBX-D-10-I-LL



Applications

- 25GBASE-ER
- CPRI Option 10/e CPRI

Standards

- SFF-8472
- SFF-8402
- SFF-8432
- SFF-8431
- CEI-28G-VSR

Features

- Up to 40 km Transmission Distance
- Low Power Consumption <1.5W
- Single 3.3V ±5% Power Supply
- LC Single Connector
- -40°C to 85°C Operating Case Temperature Range
- Compliant with SFF-8472

Description

The SFP28 transceiver is designed for use in Ethernet/eCPRI/ CPRI links up to 25Gb/s data rate and up to 40km link length. They are compliant with SFF8472, SFF-8402, SFF-8432 and applicable portions of SFF-8431. Digital diagnostics functions are available via a 2-wire serial interface, as specified in SFF-8472.

Product Specifications

I. Absolute Maximum Ratings

| Parameter | Symbol | Min. | Max. | Unit |
|----------------------------|----------|------|------|------|
| Storage Temperature | T_S | -40 | 85 | °C |
| Supply Voltage | V_{CC} | -0.3 | 3.6 | V |
| Relative Humidity | RH | 0 | 95 | % |

II. Recommended Operating Conditions

| Parameter | Symbol | Min. | Typical | Max. | Unit |
|-----------------------------------------|----------|-------|----------|-------|------|
| Operating Case Temperature Range | T_C | -40 | | 85 | °C |
| Power Supply Voltage | V_{CC} | 3.135 | 3.3 | 3.465 | V |
| Bit Rate | BR | | 25.78125 | | Gb/s |
| Max Supported Link Length | L | | | 40 | km |

III. Electrical Characteristics (T_c=-40°C to 85°C and V_{cc}= 3.135 to 3.465V)

| Parameter | Symbol | Min. | Typical | Max. | Unit | Note |
|-----------------------------------------|--------------------|-----------------|---------|----------------------|-------|------|
| Supply Voltage | V _{CC} | 3.14 | 3.3 | 3.46 | V | |
| Supply Current | I _{CC} | | | 360 | mA | |
| Transmitter | | | | | | |
| Input Differential Impedance | R _{in} | | 100 | | Ω | |
| Single Ended Data Input Swing | V _{in} | 90 | | 450 | mVp-p | |
| Transmit Disable Voltage | V _{DIS} | 2 | | V _{CCHOST} | V | |
| Transmit Enable Voltage | V _{EN} | V _{EE} | | V _{EE} +0.8 | V | |
| Transmit Fault Assert Voltage | T _{FA} | 2.2 | | V _{CCHOST} | V | |
| Transmit Fault De-Assert Voltage | V _{FDA} | V _{EE} | | V _{EE} +0.4 | V | |
| Receiver | | | | | | |
| Single Ended Data Output Swing | V _{OD} | 200 | | 450 | mVp-p | |
| LOS Fault | V _{LOSFT} | 2.2 | | V _{CCHOST} | V | |
| LOS Normal | V _{LOSNR} | V _{EE} | | V _{EE} +0.4 | V | |

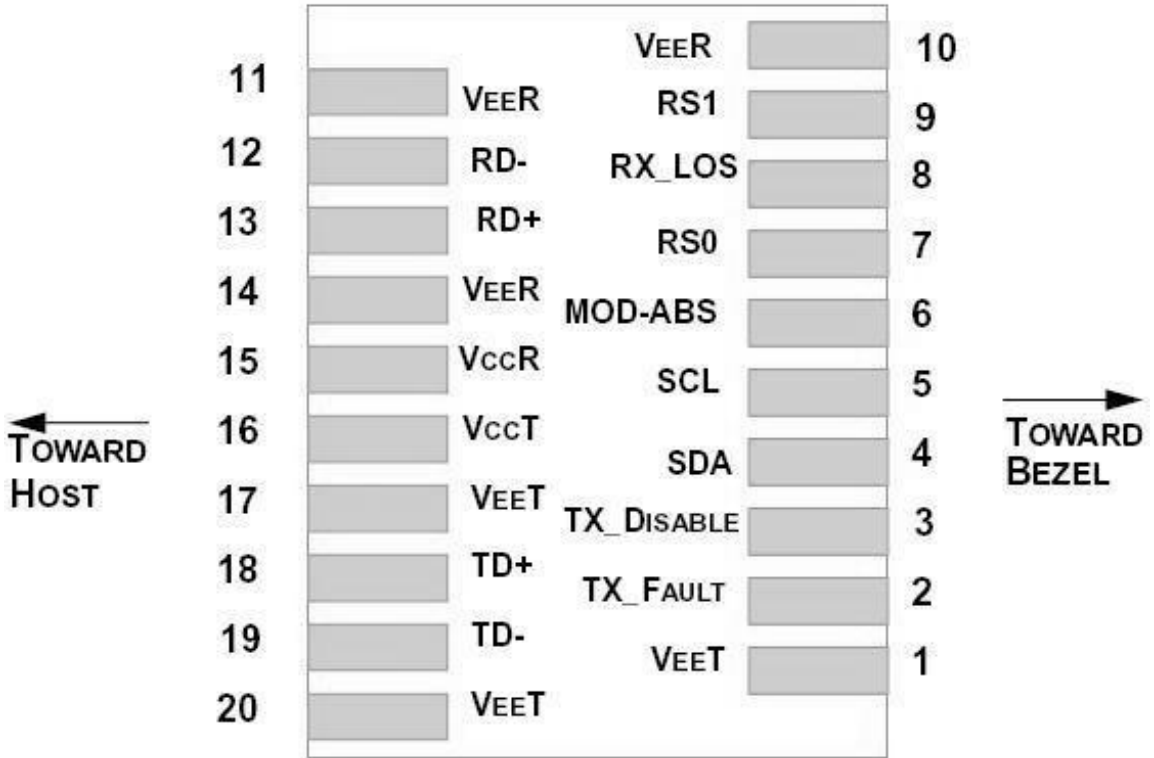
IV. Optical Parameters ($T_{OP} = 0$ to 70 °C, $V_{CC} = 3.00$ to 3.60 Volts)

| Parameter | Symbol | Min. | Typical | Max. | Unit | Note |
|------------------------------------------------|-----------|------|---------|------|------|------|
| Transmitter | | | | | | |
| Center Wavelength | λ | 1260 | 1270 | 1280 | nm | 1 |
| | | 1300 | 1310 | 1320 | nm | 2 |
| Side Mode Suppression Ratio | SMSR | 30 | | | dB | |
| Optical Output Power | P_{av} | 0 | | 6 | dBm | |
| Extinction Ratio | ER | 4 | | | dB | |
| Transmitter and Dispersion Penalty | TDP | | | 1.5 | dB | |
| Average Launch Power of OFF Transmitter | P_{OFF} | | | -30 | dBm | |
| Receiver | | | | | | |
| Center Wavelength | λ | 1300 | 1310 | 1320 | nm | 1 |
| | | 1260 | 1270 | 1280 | nm | 2 |
| Overload | | | | -6 | dBm | |
| Receiver Power | P_{av} | | | -18 | dBm | 3 |
| Assert LOS | LOS_A | -30 | | | dBm | |
| De-Assert LOS | LOS_D | | | -20 | dBm | |
| LOS Hysteresis | | 0.5 | | | dB | |

Note:

1. RTX330-238.
2. RTX330-239.
3. Measured with 25.78125Gb/s, PRBS 231-1, NRZ, ER>4dB, BER<5E-5.

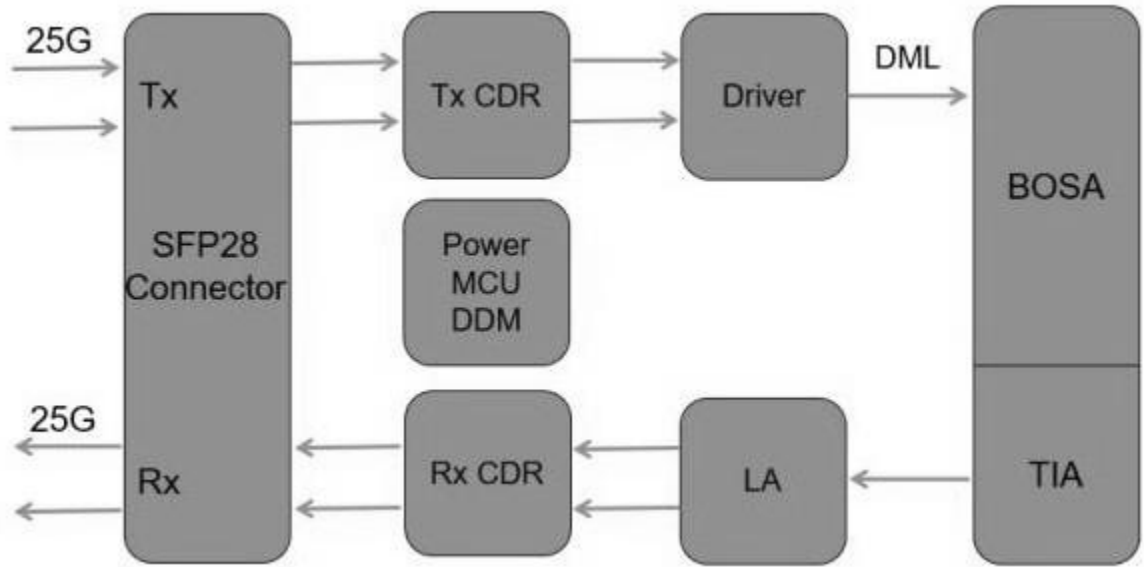
V. Pin Assignment



VI. Transceiver Pin Descriptions

| Pin No. | Symbol | Logic | Description |
|----------|------------|---------------------|----------------------------------------------------------------------------|
| 1,17,20 | $V_{EE}T$ | | Connected to signal ground on the host board |
| 2 | TX Fault | LVTTTL Output | Module transmitter fault output |
| 3 | TX Disable | LVTTTL Input | Module transmitter disable control |
| 4 | SDA | LVTTTL Input/Output | 2-wire serial interface data |
| 5 | SCL | LVTTTL Input/Output | 2-wire serial interface clock |
| 6 | MOD-ABS | | Module absent (connected to Module ground) |
| 7 | RS0 | LVTTTL Input | Rate select 0 (Rx) :Low=CDR Bypass ; High=CDR Select |
| 8 | LOS | LVTTTL Output | Receiver loss of signal |
| 9 | RS1 | LVTTTL Input | Rate select 1 (Tx) :Low=CDR Bypass ; High=CDR Select |
| 10,11,14 | $V_{EE}R$ | | Connected to signal ground on the host board. |
| 12 | RD- | CML Output | Receiver inverted data output, internally AC coupled and terminated |
| 13 | RD+ | CML Output | Receiver non-inverted data output, internally AC coupled and terminated. |
| 15 | $V_{CC}R$ | | Receiver power 3.3V supply |
| 16 | $V_{CC}T$ | | Transmitter power 3.3V supply |
| 18 | TD+ | CML Input | Transmitter non-inverted data input, internally AC coupled and terminated. |
| 19 | TD- | CML Input | Transmitter inverted data Input, internally AC coupled and terminated. |

VII. Principle Diagram



VIII. Mechanical Dimensions:

