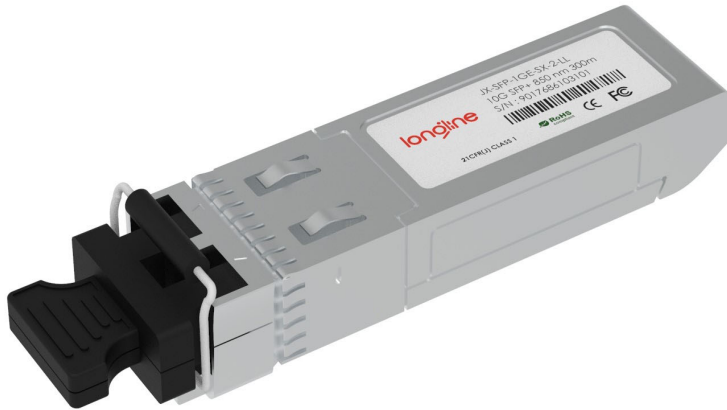


# 1000BASE-SX SFP 1310nm 2km DOM Transceiver

JX-SFP-1GE-SX-2-LL



## Application

- Fiber Channel Links
- Gigabit Ethernet Links
- Fast Ethernet Links
- Other Optical Links

## Features

- Operating data rate up to 1.25Gbps
- 1310nm FP laser transmitter
- 550m Reach for 62.5/125 $\mu$ m (550MHz.km)
- 2km Reach for 50/125 $\mu$ m (800MHz.km)
- Single 3.3V power supply and TTL Logic Interface
- Hot-pluggable SFP footprint duplex LC connector interface
- Class 1 FDA and IEC60825-1 laser safety compliant
- Commercial temperature
- Range: 0~+70°C
- Compliant with SFP MSA
- Compliant with SFF-8472
- Built-in digital diagnostic functions, including optical power monitoring

## Description

The SFP1G-SX-31 series multi-mode transceiver is small form factor pluggable module for bi-directional serial optical data communications such as Ethernet and SDH/SONET. It is with the SFP 20-pin connector to allow hot plug capability. This module is designed for multi-mode fiber and operates at a nominal wavelength of 1310nm.

The transmitter section uses a multiple quantum well 1310nm laser and is a class 1 laser compliant according to International Safety Standard IEC 60825. The receiver section uses an integrated GaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC. The SFP1G-SX-31 series are designed to be compliant with SFF-8472 MSA.

## Product Specifications

### I. General Product Characteristics

Parameter	Symbol	Min	Typ.	Max	Unit
<b>Bit Rate</b>	BR			1.25	Gb/s
<b>Max. Supported Link Length</b>	$L_{MAX}$			2	km

### II. Absolute Maximum Ratings

\*Exceeding any one of these values may destroy the device immediately

Parameter	Symbol	Min	Max	Units
<b>Storage Temperature</b>	$T_s$	-40	+85	°C
<b>Supply Voltage</b>	$V_{cc}$	-0.5	3.6	V
<b>Operating Relative Humidity</b>		-	95	%

### III. Optical and Electrical Characteristics

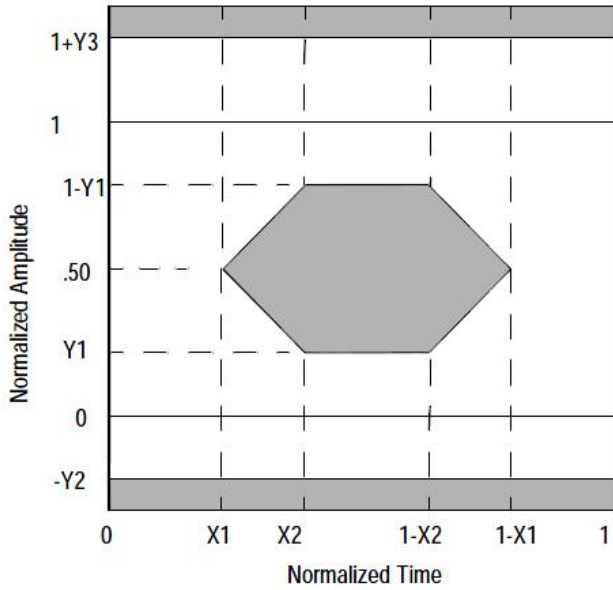
Parameter	Symbol	Min	Typ.	Max	Unit	Notes
<b>Transmitter</b>						
<b>LVPECL Inputs(Differential)</b>	Vin	500		2400	mVpp	AC coupled inputs*(Note1)
<b>Input Impedance (Differential)</b>	Zin	85	100	115	ohms	Rin > 100 kohms @ DC
<b>TX_Dis</b>	Disable	2		Vcc		
	Enable	0		0.8		
<b>TX_FAULT</b>	Fault	2		Vcc+0.3		
	Normal	0		0.5		
<b>Receiver</b>						
<b>LVPECL Outputs (Differential)</b>	Vout	370		2000	mVpp	AC coupled outputs*(Note1)
<b>Output Impedance (Differential)</b>	Zout	85	100	115	ohms	
<b>RX_LOS</b>	LOS	2		Vcc+0.3	V	
	Normal	0		0.8	V	
<b>MOD_DEF (0:2)</b>	VoH	2.5			V	With Serial ID
	VoL	0		0.5	V	

## IV. Optical Characteristics

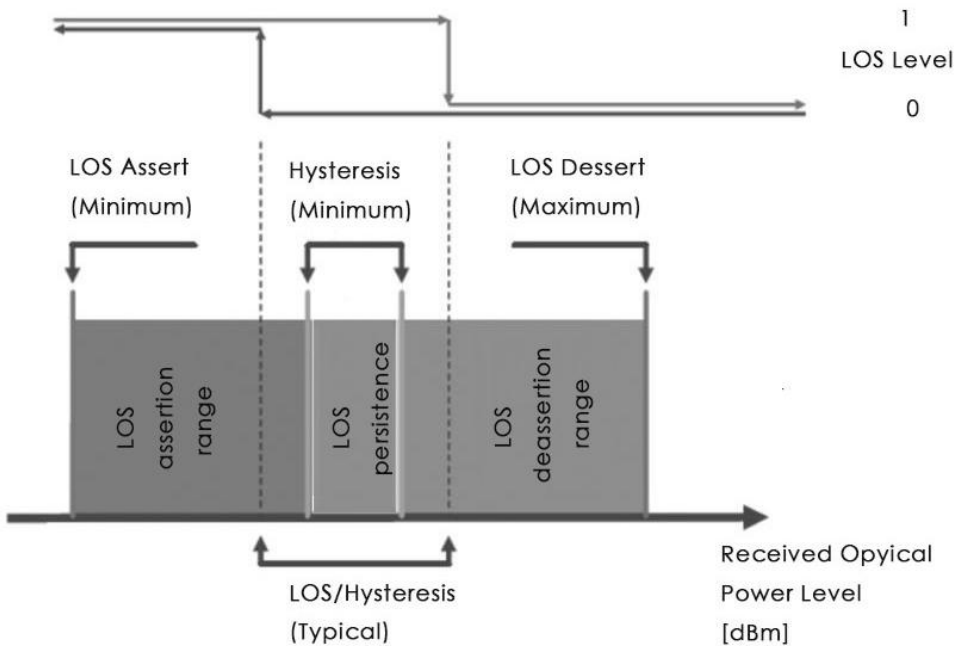
Parameter	Symbol	Min	Typ.	Max	Unit
<b>50<math>\mu</math>m Core Diameter MMF(800MHz.km)</b>	L		1000		m
<b>62.5<math>\mu</math>m Core Diameter MMF(550MHz.km)</b>	L		550		m
<b>Date Rate</b>			1.25		Gbps
<b>Transmitter</b>					
<b>Center Wavelength</b>	$\lambda_c$	1260	1310	1360	nm
<b>Spectral Width (RMS)</b>	$\Delta\lambda$			5	nm
<b>Average Output Power<sup>*(note2)</sup></b>	Pout	-9		-3	dBm
<b>Extinction Ratio<sup>*(note3)</sup></b>	ER	9			Db
<b>Rise/Fall Time(20%~80%)</b>	tr/tf			0.26	ns
<b>Total Jitter<sup>*(note3)</sup></b>	TJ			0.43	UI
<b>Output Optical Eye<sup>*(note3)</sup></b>	IEEE802.3ah-2004 Compliant <sup>*(Note5)</sup>				
<b>TX_Disable Assert Time</b>	t_off			10	us
<b>Receiver</b>					
<b>Center Wavelength</b>	$\lambda_c$	1260		1600	nm
<b>Receiver Sensitivity<sup>*(note4)</sup></b>	Pmin			-21	dBm
<b>Receiver Overload</b>	Pmax	-3			dBm
<b>Return Loss</b>		12			DB
<b>LOS De-Assert</b>	LOSD			-22	dB
<b>LOS Assert</b>	LOSA	-35			dBm
<b>LOS Hysteresis<sup>*(note6)</sup></b>		0.5			DB

**Notes:**

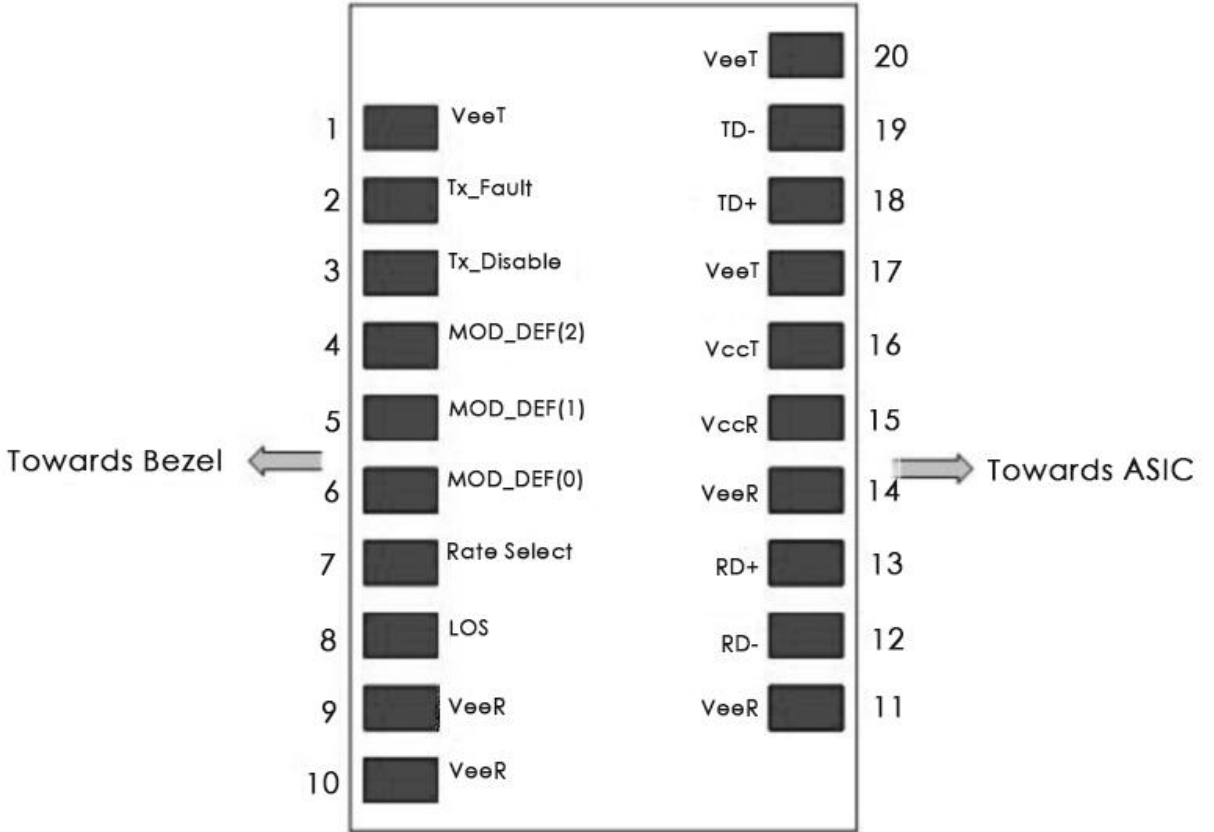
1. LVPECL logic, internally AC coupled.
2. Output is coupled into a 62.5/125 mm multi-mode fiber.
3. Filtered, measured with a PRBS 2<sup>7</sup>-1 test pattern @1.25Gbps
4. Minimum average optical power measured at BER less than 1E-12, with a 2<sup>7</sup>-1 PRBS and ER=9 Db.
5. Eye Pattern Mask



**6.LOS Hysteresis**



### IV. Pin Description



Pin Num.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	Note5
2	TX Fault	Transmitter Fault Indication	3	Note 1
3	TX Disable	Transmitter Disable	3	Note 2 Module disables on high or open
4	MOD-DEF2	SDA	3	Note 3 2 wire serial ID interface.
5	MOD-DEF1	SCL	3	Note 3 2 wire serial ID interface.
6	MOD-DEF0	MOD_ABS	3	Note 3 Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	Note 4
9	VeeR	Receiver Ground	1	Note 5
10	VeeR	Receiver Ground	1	Note 5
11	VeeR	Receiver Ground	1	Note 5
12	RD-	Inv. Received Data Out	3	Note 6
13	RD+	Received Data Out	3	Note 7
14	VeeR	Receiver Ground	1	Note 5
15	VccR	Receiver Power	2	Note 7 3.3V $\pm$ 5%
16	VccT	Transmitter Power	2	Note 7 3.3V $\pm$ 5%
17	VeeT	Transmitter Ground	1	Note 5
18	TD+	Transmit Data In	3	Note 8
19	TD-	Inv. Transmit Data In	3	Note 8
20	VeeT	Transmitter Ground	1	Note 5

**Notes:**

1. TX Fault is an open collector/drain output, which should be pulled up with a 4.7K - 10K $\Omega$  resistor on the host board. Pull up voltage between 2.0V and VccT, R+0.3V. When high, output indicates a laser fault of some kinds. Low indicates normal operation. In low state, the output will be pulled to < 0.8V.
2. TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7 – 10 K $\Omega$  resistor. Its states are: Low (0 – 0.8V): Transmitter on (>0.8, < 2.0V): Undefined High (2.0 – 3.465V): Transmitter Disabled Open: Transmitter Disabled
3. Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7 – 10 K $\Omega$  resistor on the host board. The pull-up voltage shall be VccT or VccR . Mod-Def 0 is grounded by the module to indicate that the module is present Mod-Def 1 is the clock line of two wire serial interface for serial ID Mod-Def 2 is the data line of two wire serial interface for serial ID
4. LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10K $\Omega$  resistor. Pull up voltage between 2.0V and VccT/R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.
5. VeeR and VeeT may be internally connected within the SFP module.
6. RD-/+: These are the differential receiver outputs. They are AC coupled 100 $\Omega$  differential lines which should be terminated with 100 $\Omega$  (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required the host board. The voltage swing on these lines will be between 400 and 2000 mV differential (200 – 1000 mV single ended) when properly terminated.
7. VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V  $\pm$  5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.
8. TD-/+: TD-/+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100 $\Omega$  differential termination inside the module. The AC coupling is done inside the module and is thus not required on the host board. The inputs will accept differential swings of 400–2000mV (200–1000mV single-ended).



## V. Mechanical Specifications

